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METHODOLOGY

Building ASIPs: The Mescal Methodology offers a straightforward nevertheless full methodology for the design of these software-specific instruction processors (ASIPs). The key elements of this technique are: Judiciously using benchmarking. Inclusively determining the architectural space. Successfully describing and evaluating the ASIPs

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Kurt Keutzer grew up in Indianapolis, Indiana. [citation needed] He earned a bachelor's degree in mathematics from Maharishi University of Management (formerly Mararishi International University) in 1978, and a PhD in computer science from Indiana University in 1984. Career. Keutzer joined Bell Labs in 1984, where he worked on logic synthesis.

Kurt Keutzer - Wikipedia

The MESCAL methodology and the Tensilica configurable-processor-core design approach agree on the importance of processors as the key flexible building block for SoC designs,

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making it possible to leverage the very high transistor count and excellent connectivity made available by nanometer silicon lithography with relatively little manual design effort.

Commercial Configurable Processors and the MESCAL Approach ...

An application-specific instruction set processor (ASIP) is a component used in system-on-a-chip design. The instruction set of an ASIP is tailored to benefit a specific application. This specialization of the core provides a tradeoff between the flexibility of a general purpose CPU and the performance of an ASIC.. Some ASIPs have a configurable instruction set.

Application-specific instruction set processor - Wikipedia

Building ASIPs: The Mescal Methodology, Matthias Gries, Kurt Keutzer, Springer Publishers, 2005, 0387260579 Methods for Evaluating and Covering the Design

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Space during Early Design Development, Matthias Gries, Integration, the VLSI Journal, Elsevier, 38, 2, 131-183, December, 2004

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